IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re F	Pate	nt A	pplica	tion of:			
Takahisa HATAKEYAMA et al.							
Applic	atio	n No	o.: (U	nassigned)	Group Art Unit:		
Filed:	Jul	y 9,	2003		Examiner:		
For:	OF	PEN	GEN	ERIC TAMPER RESISTANT CF	PU AND APPLICATION SYSTEM THEREOF		
				INFORMATION DISCLOS	SURE STATEMENT		
PO Bo	ox 1	450		Patents 13-1450			
Sir:							
the su	led o ibjeo	certa	iin info S. pat	ormation which the Examiner m	ovisions of 37 CFR § 1.56, there is hereby ay consider material to the examination of that the Examiner make this information of the subject application.		
1. Enclosures		Encl	osures accompanying this Infor	companying this Information Disclosure Statement are:			
				application or a PCT Internatio English language translation (deach non-English language pu Explanations of Relevancy of F	complete or relevant portion(s)) attached to		
	2.	\boxtimes			a concise explanation of what is presently each non-English language publication is		
		2a.		enclosed "English-language ve indicates the degree of relevar 609, Minimum Requirements for	s 2a, 2b, 2c and/or 2d) sh language publications were cited on the ersion of the search report or action which nce found by the foreign office". (See MPEP or an Information Disclosure Statement, Part Relevance, pp. 600-100 to 600-101, Rev. 1,		
		2b.		set forth in the application.			

portion(s)) is attac	an English language translation (complete or relevant ched to each non-English language publication. hment 1(e), hereto.
to be, material to patentabi (other than search report(s	the information cited in this Statement is, or is considered lity nor a representation that a search has been made) from a counterpart foreign application or a PCT t, if submitted herewith). 37 CFR §§ 1.97(g) and (h).
	Respectfully submitted,
	STAAS & HALSEY LLP
Dated: 7/6/3 1201 New York Ave., N.W., Suite 700 Washington, D.C. 20005 Telephone: (202) 434-1500 Facsimile: (202) 434-1501	By: Market Surface Sur

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

ATTORNEY DOCKET NO.	APPLICATION NO.				
826.1880	(Unassigned)				
FIRST NAMED INVENTOR					
Takahisa HATAKEYAMA et al.					
FILING DATE	GROUP ART UNIT				
July 9, 2003	GROUP ART ONT				

U.S. PATENT DOCUMENTS

				110111 000011101110			
EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA	2001/0018736	08/30/01	Hashimoto et al.			
	AB	2001/0014157	08/16/01	Hashimoto et al.			
	AC	2003/0050894	03/13/03	Kambayashi et al.			
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS

		1 01/2	OIL WITH BOOMETING				
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLA YES	NO NO
 AG	03083132	04/09/91	Japan			Abstract	
АН	2001-230770	08/24/01	Japan				Х
Al	2000-260121	09/22/00	Japan				Х
AJ							
AK							
AL							

OTHER REFERENCES (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

AM	Dallas Semiconductor: DS5002FP Secure Microprocessor Chip, pp. 1-29
AN	Anderson, "Tamper Resistance - A Cautionary Note", <u>USENIX Association</u> , pp. 1-11 (1996)
AO	

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ATTACHMENT 1(e)

EXPLANATIONS OF RELEVANCY OF REFERENCES

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References AA and AB correspond to Reference AH and provides the translation

Reference AC correspond to Reference AI and provides the translation